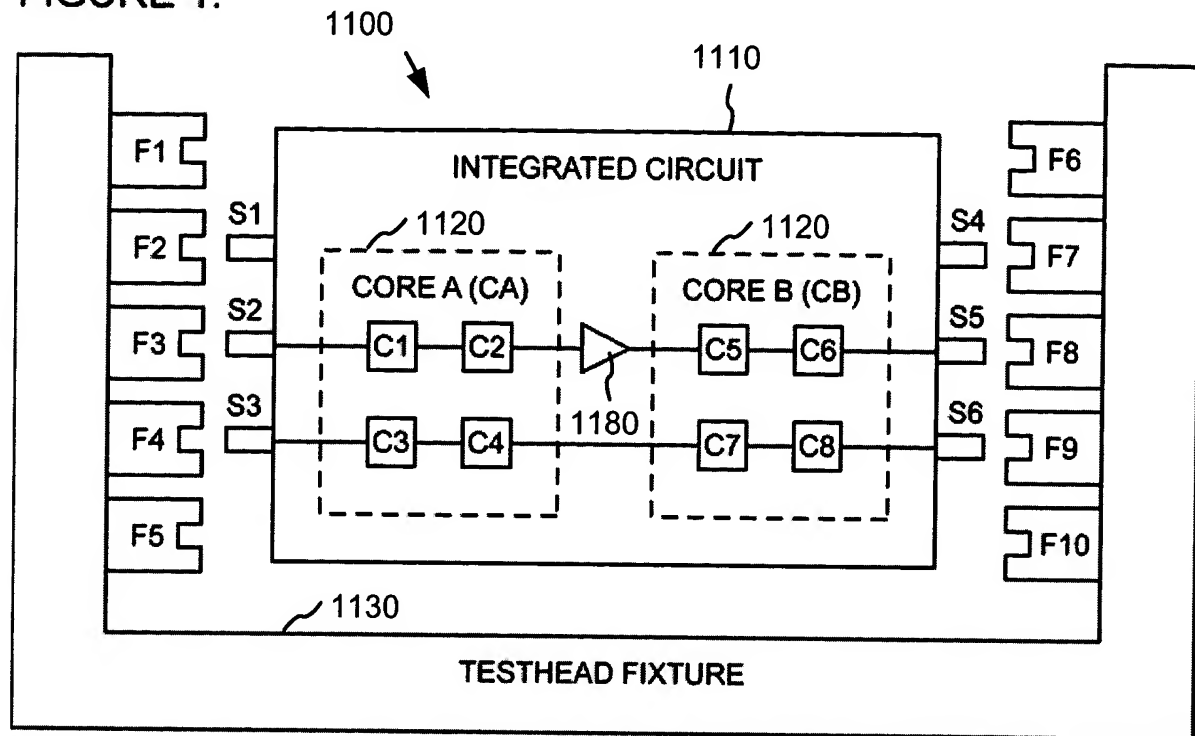


FIGURE 1.



1140	
SIGNAL LIST	
S1	INPUT
S2	INPUT, SCAN-INPUT
S3	INPUT, SCAN-INPUT
S4	OUTPUT
S5	OUTPUT, SCAN-OUTPUT
S6	OUTPUT, SCAN-OUTPUT

FIXTURE PIN MAP	
F2	S1
F3	S2
F4	S3
F7	S4
F8	S5
F9	S6

SCAN CHAINS	
SC1	SCAN-INPUT = S2, SCAN-OUTPUT = S5, SCAN-CELLS = CA.C1 CA.C2 ! CB.C5 CB.C6
SC2	SCAN-INPUT = S3, SCAN-OUTPUT = S6, SCAN-CELLS = CA.C3 CA.C4 CB.C7 CB.C8

1160

1150

FIGURE 2.

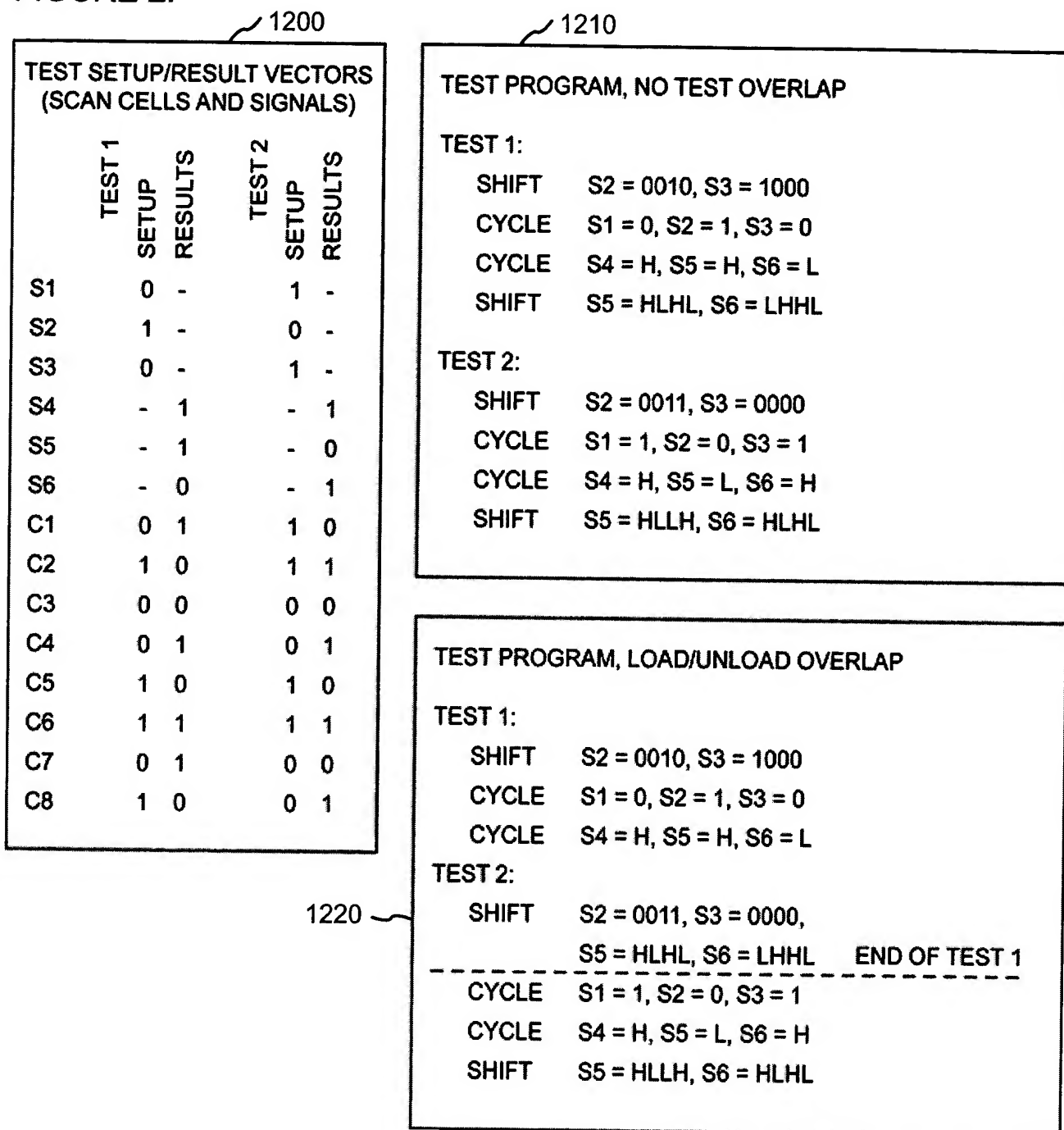


FIGURE 3.

1300

CYCLIZED TEST PROGRAM																
CYCLE																
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
S1	-	-	-	-	0	-	-	-	-	-	1	-	-	-	-	-
S2	0	0	1	0	1	-	0	0	1	1	0	-	-	-	-	-
S3	1	0	0	0	0	-	0	0	0	0	1	-	-	-	-	-
S4	-	-	-	-	-	H	-	-	-	-	-	H	-	-	-	-
S5	-	-	-	-	-	H	H	L	H	L	-	L	H	L	L	H
S6	-	-	-	-	-	L	L	H	H	L	-	H	H	L	H	L
---	TEST 1 LOAD				---	TEST 1 LAUNCH			---	TEST 2 LOAD		---	TEST 2 LAUNCH			
---					---	TEST 1 CAPTURE			---			---	TEST 2 CAPTURE			
---					---	TEST 1 UNLOAD /			---			---	TEST 2 UNLOAD			

FIGURE 4.

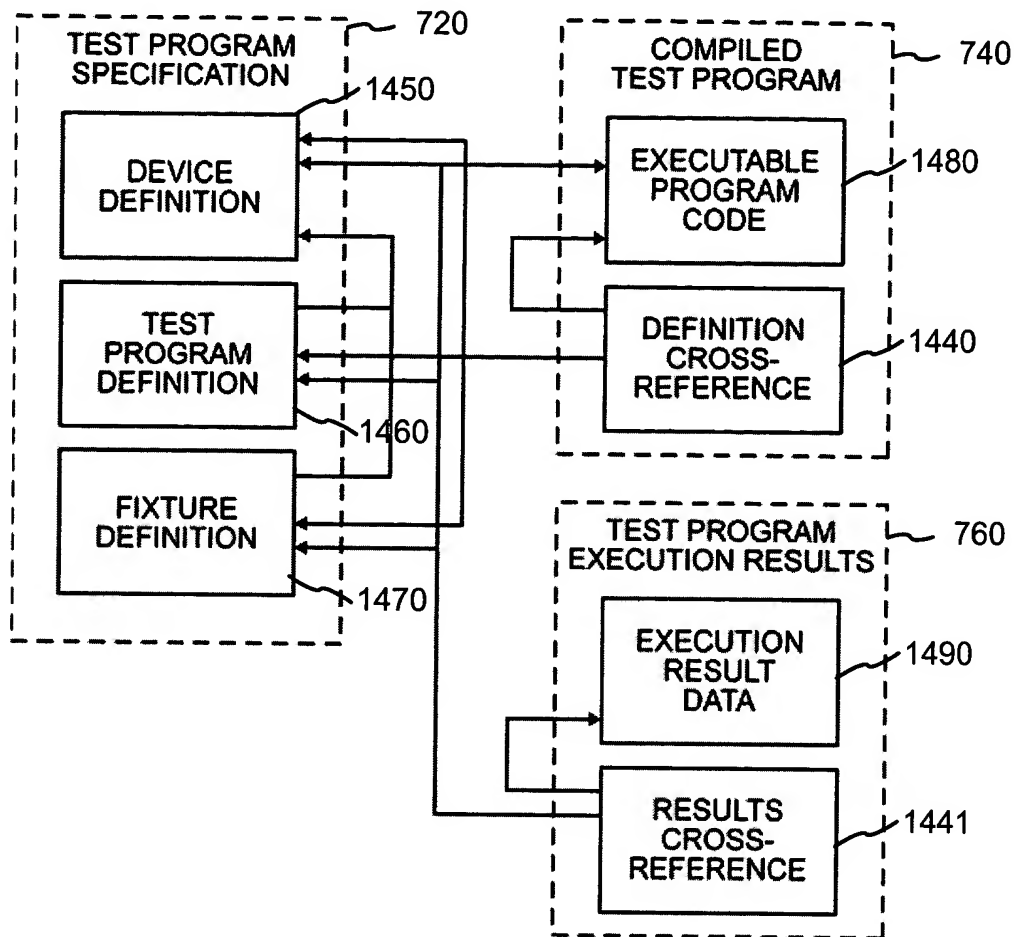


FIGURE 5.

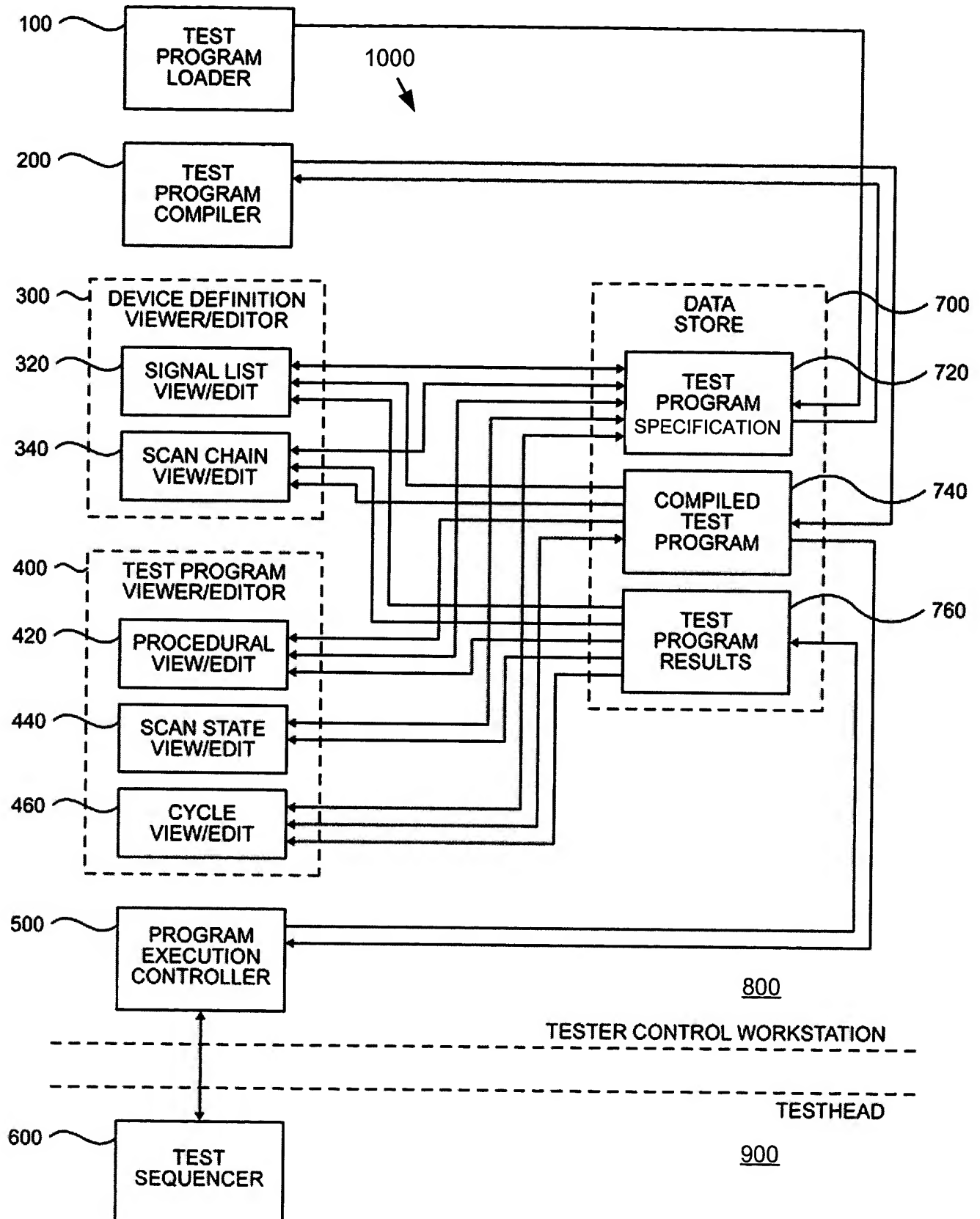


FIGURE 6.

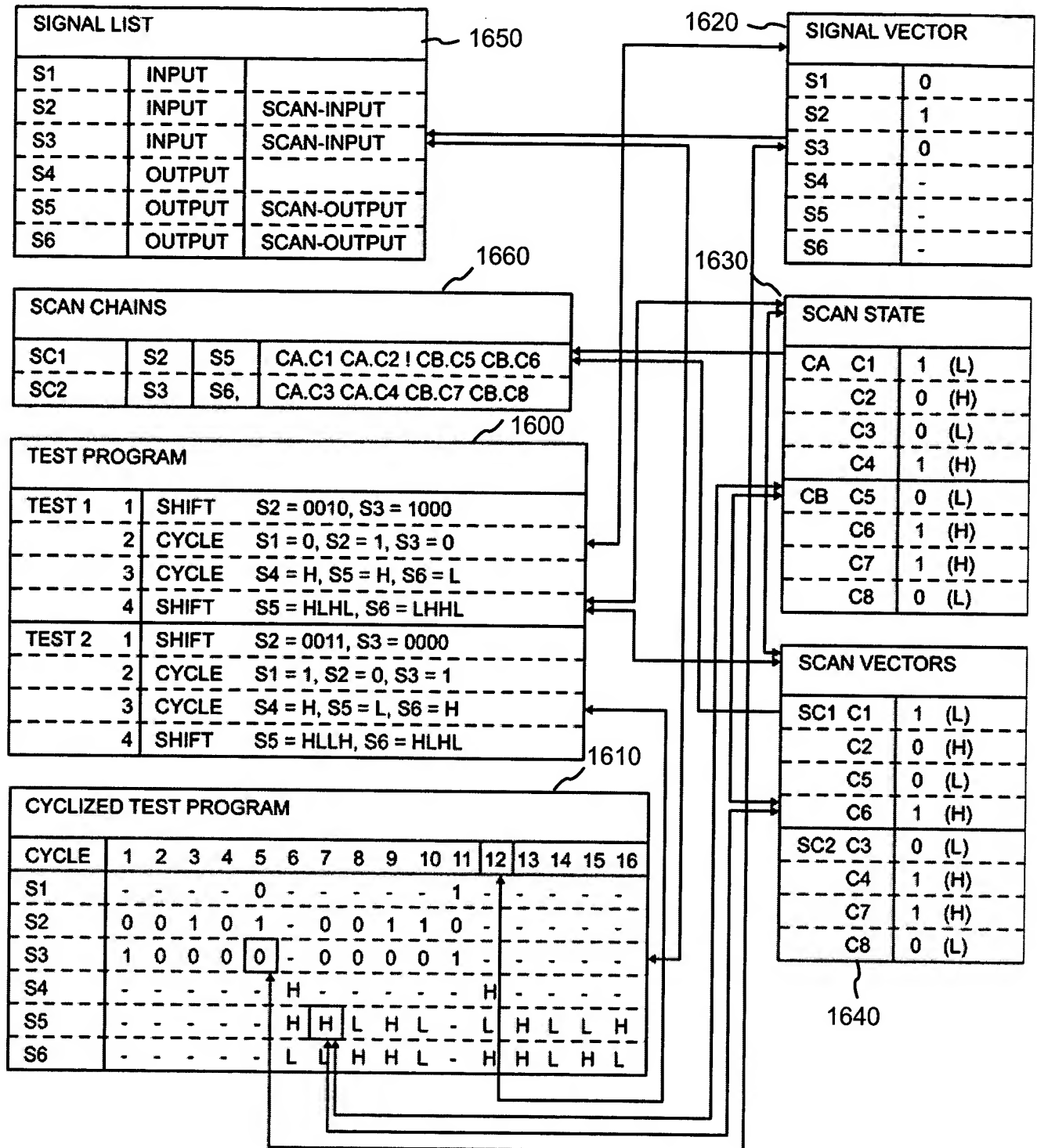


FIGURE 7.

